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Amendment to the Specification:

Replace the two paragraphs beginning on page 1, line 26
as follows:

--In one embodiment of the present invention, a method of verifying an incremental change to an integrated circuit design includes steps of[[: (a)]] receiving as input an integrated circuit design database and[[; (b)]] ~~receiving as input~~ an engineering change order. Objects[[; (c)]] ~~identifying and marking objects~~ in the integrated circuit design database are identified and marked to indicate a current state of the integrated circuit design database. The[[; (d)]] ~~applying the~~ engineering change order is applied to the integrated circuit design database, and[[; (e)]] ~~analyzing~~ the integrated circuit design database is analyzed to generate a list of incremental changes to the integrated circuit design database resulting from the engineering change order. Objects[[; (f)]] ~~identifying and marking objects~~ in the integrated circuit design database included in the list of incremental changes are identified and marked to distinguish objects in the integrated circuit design database that were changed from the current state. The marked[[; and]] ~~(g) streaming out the~~ integrated circuit design database distinguishing the objects that were changed from the current state is generated as output.

In another embodiment of the present invention, a computer program product for verifying an incremental change to an integrated circuit design that includes[[:] a medium for embodying a computer program for input to a computer[[;]] and a computer program embodied in the medium. The computer program causes ~~for causing~~ the computer to perform steps of[[: (a)]] receiving as input an integrated circuit design database

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and[[; (b)]] ~~receiving as input~~ an engineering change order. Objects [[; (c)]] ~~identifying and marking objects~~ in the integrated circuit design database are identified and marked to indicate a current state of the integrated circuit design database. The[[; (d)]] ~~applying the~~ engineering change order is applied to the integrated circuit design database, and[[; (e)]] ~~analyzing~~ the integrated circuit design database is analyzed to generate a list of incremental changes to the integrated circuit design database resulting from the engineering change order. Objects[[; (f)]] ~~identifying and marking objects~~ in the integrated circuit design database included in the list of incremental changes are identified and marked to distinguish objects in the integrated circuit design database that were changed from the current state. The marked[[; and]] ~~(g) streaming out the~~ integrated circuit design database distinguishing the objects that were changed from the current state is generated as output.--

Replace the paragraph beginning on page 13, line 5 as follows:

--In the design streamout block 208, the physical representation of the integrated circuit design is translated from into a GDSII (generic data stream) file.--

Replace the Abstract beginning on page 23, line 3 as follows:

--A method and computer program product for verifying an incremental change to an integrated circuit design are described that include steps of[[; (a)]] receiving as input an integrated circuit design database and[[; (b)]] ~~receiving as input~~ an engineering change order. Objects[[; (c)]] ~~identifying and marking objects~~ in the integrated

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circuit design database are identified and marked to indicate a current state of the integrated circuit design database. The ~~[[; (d)]] applying the engineering change order is applied~~ to the integrated circuit design database, and ~~[[; (e)]] analyzing the integrated circuit design database is analyzed~~ to generate a list of incremental changes to the integrated circuit design database resulting from the engineering change order. Objects ~~[[; (f)]] identifying and marking objects~~ in the integrated circuit design database included in the list of incremental changes are identified and marked to distinguish objects in the integrated circuit design database that were changed from the current state. The marked ~~[[;]] and (g) streaming out the~~ integrated circuit design database distinguishing the objects that were changed from the current state is generated as output.--